

REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	MARK THOREN	03-25-15

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH FINISHED COPPER THICKNESS 2.0 OZ. OUTER LAYERS, 1.0 OZ. INNER LAYERS.
STACKUP AS PER FIG.1, "LAYER STRUCTURE".
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES SHALL BE 6 MILS DIAMETER TRUE POSITION.
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-SILKSCREEN LEGENDS SHALL BE WHITE NON-CONDUCTIVE EPOXY INK.
-PLATING SHALL BE IMMERSION GOLD BOTH SIDES.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- BOARDS SHALL BE RoHS COMPLIANT.
- BOARDS SHALL BE PANELIZED USING ROUTE AND RETAIN METHOD.
THERE SHALL BE TWO RETAINING TABS ON ALL BOARD EDGES GREATER THAN 2.5 INCHES LONG.
- VIAS, QTY 7, 13 MILS DIAMETER, SHALL BE FILLED AND CAPPED.
- ANY ELECTRICAL TEST/QA STAMPS SHALL BE ON BOTTOM SIDE.

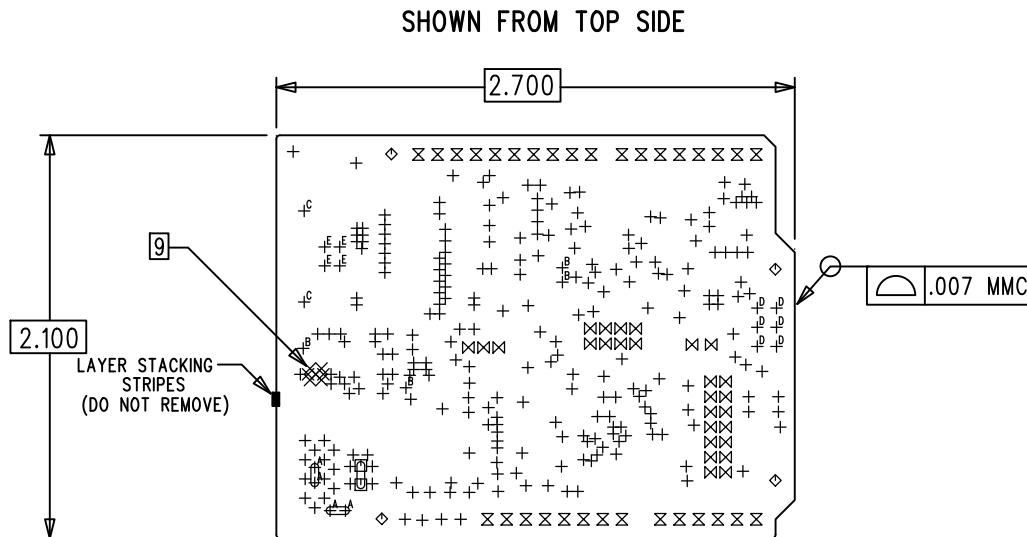
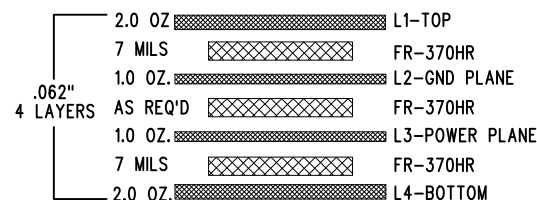
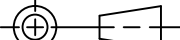



FIG. 1
LAYER STRUCTURE



SIZE	QTY	SYM	PLATED	TOL
12	252	+	YES	+/- 3 MILS
40 x 120	2	+	YES	+/- 3 MILS
40 x 140	1	+	YES	+/- 3 MILS
126	4	◇	NO	+/- 3 MILS
40	32	⊗	YES	+/- 3 MILS
35	27	⊗	YES	+/- 3 MILS
13	7	⊗	YES	+/- 3 MILS
30	4	+	YES	+0.003/-0.000
90	2	+	YES	+/- 3 MILS
37	6	+	YES	+/- 3 MILS
36	4	+	YES	+/- 3 MILS

DRILL CHART

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION 	APPROVALS		 LINEAR TECHNOLOGY	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY		
	PCB DES.	M. HAWKINS				
	APP ENG.	MARK THOREN	TITLE: FABRICATION DRAWING LINDUINO ONE: LINEAR TECHNOLOGY ISOLATED ARDUINO-COMPATIBLE DEMONSTRATION BOARD			
			SIZE	IC NO. LTM2884CY	REV	
			N/A	DEMO CIRCUIT 2026C	1	
	SCALE = NONE		FILENAME: DC2026C-1.PCB		SHT 1 OF 1	